

## Letter of Volatility

The table below provides volatility information and memory types for the Vertiv Cybex Secure KVM as part of the documentation required for compliance with NIAP Peripheral Sharing Switch Protection Profile Rev 3.0. Please note that there are no remnants of user data retained in the device when the power is turned off.

Product Model	No. of devices in each product	Function, MFR and P/N	Storage Type	Size	Volatility	Contains User Data	
IS100 IS100D IS110 IS110D	1	System Controller, Host emulators; ST Microelectronics STM32F2071CH6	Embedded SRAM <sup>1</sup>	128KB	Volatile	May contain user data	
			Embedded Flash <sup>2</sup>	256KB	Non-Volatile	No user data	
			Embedded EEPROM	4 KB	Non-Volatile	No user data	
			OTP Memory	512 bytes	Non-Volatile	No user data	
	0 or 1	Video Controller; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	No user data	
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data	
			Embedded EEPROM	4 KB	Non-Volatile	No user data	
	0 or 1	Device emulators; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	May contain user data	
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data	
			Embedded EEPROM	4 KB	Non-Volatile	No user data	
	1	1	RTC (Real Time Clock); Intersil; ISL1209IU10Z-TK	RAM <sup>3</sup>	2 Bytes	Non-Volatile <sup>2</sup>	No user data
	1	1	Anti-tampering; Atmel; AT88SC0204CA-SU	Crypto memory EEPROM <sup>4</sup>	256 Byte	Non-Volatile	No user data
	0/1	0/1	EDID Emulator; ST Microelectronics M24C02-WMN6TP	EEPROM <sup>5</sup>	2 KB	Non-Volatile	No user data
SC820 SC820D SCKM120 SC820H SCM120 SCM120H	1	System Controller, Host emulators; ST Microelectronics STM32F2071CH6	Embedded SRAM <sup>1</sup>	128KB	Volatile	May contain user data	
			Embedded Flash <sup>2</sup>	256KB	Non-Volatile	No user data	
			Embedded EEPROM	4 KB	Non-Volatile	No user data	

SC920H SC920D SC920 SC920XD			OTP Memory	512 bytes	Non-Volatile	No user data
	1	Video Controller; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	No user data
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
	2	Device emulators; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	May contain user data
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
	1	RTC (Real Time Clock); Intersil; ISL1209IU10Z-TK	RAM <sup>3</sup>	2 Bytes	Non-Volatile <sup>2</sup>	No user data
	1	Anti-tampering; Atmel; AT88SC0204CA-SU	Crypto memory EEPROM <sup>4</sup>	256 Byte	Non-Volatile	No user data
	2	EDID Emulator; ST Microelectronics M24C02-WMN6TP	EEPROM <sup>5</sup>	2 KB	Non-Volatile	No user data
SC840 SC845 SC945 SCKM140 SCKM145 SC940 SC840D SC845D SC940D SC945D SC840H SC845H SC940H SC945H SC945XD SCM145 SCM145H SC1045XD	1 or 2	System Controller, Host emulators, DPP Controller (optional); ST Microelectronics STM32F207ICH6	Embedded SRAM <sup>1</sup>	128KB	Volatile	May contain user data
			Embedded Flash <sup>2</sup>	256KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
			OTP Memory	512 bytes	Non-Volatile	No user data
	1 in SH or 2 in DH models	Video Controller; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	No user data
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
	4 in SH or 8 in DH models	Device emulators; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	May contain user data
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
	1	RTC (Real Time Clock); Intersil; ISL1209IU10Z-TK	RAM <sup>3</sup>	2 Bytes	Non-Volatile <sup>2</sup>	No user data

	1	Anti-tampering; Atmel; AT88SC0204CA-SU	Crypto memory EEPROM <sup>4</sup>	256 Byte	Non-Volatile	No user data
	4 in SH or 8 in DH models	EDID Emulator; ST Microelectronics M24C02-WMN6TP	EEPROM <sup>5</sup>	2 KB	Non-Volatile	No user data
MV145D MV245 MV125H MV245D	1	System Controller, Host emulators; ST Microelectronics STM32F2071CH6	Embedded SRAM <sup>1</sup>	128KB	Volatile	May contain user data
			Embedded Flash <sup>2</sup>	256KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
			OTP Memory	512 bytes	Non-Volatile	No user data
	1	Video Controller configuration memory; Spansion S25FL512SAGM	Flash <sup>6</sup>	512Mb	Non-Volatile	No user data
	2 or 4	Device emulators; ST Microelectronics STM32L151CUB6	Embedded SRAM <sup>1</sup>	16KB	Volatile	May contain user data
			Embedded Flash <sup>2</sup>	128KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
	1	RTC (Real Time Clock); Intersil; ISL1209IU10Z-TK	RAM <sup>3</sup>	2 Bytes	Non-Volatile <sup>2</sup>	No user data
	1	Anti-tampering; Atmel; AT88SC0204CA-SU	Crypto memory EEPROM <sup>4</sup>	256 Byte	Non-Volatile	No user data
2 or 4	EDID Emulator; ST Microelectronics M24C02-WMN6TP	EEPROM <sup>5</sup>	2 KB	Non-Volatile	No user data	
SC4MDS SC4MDST SC2MDS SC2MDST	1	System Controller; ST Microelectronics STM32F2071CH6	Embedded SRAM	128KB	Volatile	No user data
			Embedded Flash	256KB	Non-Volatile	No user data
			Embedded EEPROM	4 KB	Non-Volatile	No user data
			OTP Memory	512 bytes	Non-Volatile	No user data
	1	Anti-tampering; Atmel; AT88SC0204CA-SU	Crypto memory EEPROM <sup>4</sup>	256 Byte	Non-Volatile	No user data



**Notes:**

<sup>1</sup> SRAM stores USB Host stack parameters and up to 4 last key-codes. Data is erased when the KVM is being powered off. It is also erased whenever the user switches channels. Device emulators are powered by the individual connected computers and therefore devices are powered as long as the connected computer is powered.

<sup>2</sup> Flash is used to store firmware code and contains no user data. Flash is permanently locked by fuses after initial programming to prevent rewriting (becoming ROM). It is an integral part of the ST Microcontroller together with SRAM and EEPROM.

<sup>3</sup> Memory is volatile but it is powered from a battery backup. It contains anti-tampering unique device authentication keys and events log.

<sup>4</sup> Crypto memory is used to store device unique authentication keys for anti-tampering.

<sup>5</sup> EEPROM is used to store operational parameters (display Plug & Play) and contains no user data. These devices are powered by the individual computers connected to the TOE and therefore are powered as long as powered computer is connected.

<sup>6</sup> Configuration flash is used to store FPGA code. It may also store custom background bitmaps used in Combiner KVM.

If any additional information is required, please contact me directly:

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Date: October 13, 2017, Revision C  
Document No. HDC10331